

S/N 09/880699

PATENT

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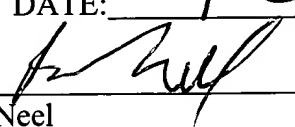
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Richard Takahashi
Serial No.: 09/880699
Filed: June 13, 2001
Title: METHOD AND APPARATUS FOR CREATING A MESSAGE DIGEST
USING A PARALLEL, ONE-WAY HASH ALGORITHM

Examiner: Unknown
Group Art Unit: 2131
Docket: 4224-20US1

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date set forth below as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Alexandria, VA 22313-1450

DEPOSIT DATE: 1-26-04


Bruce T. Neel
Reg. No. 37,406

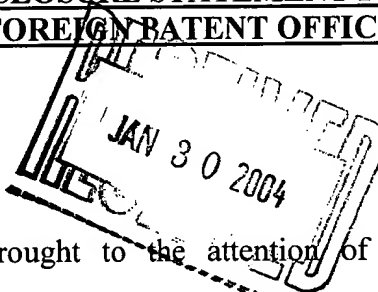
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**INFORMATION DISCLOSURE STATEMENT FOR
ART CITED BY A FOREIGN PATENT OFFICE**

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



The enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure

Statement. Accordingly, it is believed that no fee is required with this Information .
Disclosure Statement. A copy of the pertinent communication from the foreign patent
office is enclosed.

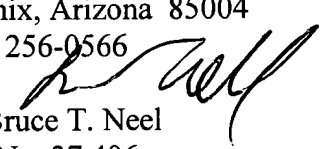
The Examiner is invited to contact the Applicants' Representative at the below-
listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

Richard Takahashi

By his Representative,
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Date: 1-26-04


By: Bruce T. Neel
Reg. No. 37,406



Sheet 1 of 1

Form 1449*

Atty. Docket No.: 4224-20US1

Serial No. 09/880699

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**
(Use several sheets if necessary)

Applicant: Richard Takahashi

Filing Date: 6/13/01

Group: 2131

U.S. PATENT DOCUMENTS

**EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	

FOREIGN PATENT DOCUMENTS

**EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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OTHER DOCUMENTS

**EXAMINER INITIAL (Including Author, Title, Date, Pertinent Pages, Etc.)

_____ BOSSELAERS A ET AL: "SHA: A Design for Parallel Architectures?"
ADVANCES IN CRYPTOLOGY. EUROCRYPT 97, 25 February 1997 (1997-02-25),
pages 1-15; Konstanz, Germany.

_____ SHERIGAR M B ET AL: "A pipelined parallel processor to implement MD4 message
digest algorithm on Xilinx FPGA"
VLSI DESIGN, 1998. PROCEEDINGS., 1998 ELEVENTH INTERNATIONAL
CONFERENCE ON CHENNAI, INDIA 4-7 JAN. 1998, LOS ALAMITOS, CA, USA,
IEEE COMPUT. SOC, US. 4 January 1998 (1998-01-04), pages 394-399.

_____ ARNOLD J M: "Mapping the MD5 hash algorithm onto the NAPA architecture"
FPGAS FOR CUSTOM COMPUTING MACHINES, 1998. PROCEEDINGS. IEEE
SYMPOSIUM ON NAPA VALLEY, CA, USA 15-17 APRIL 1998, LOS
ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 15 April 1998 (1998-04-15),
pages 267-268.

Examiner

Date Considered

* Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;
Draw line through citation if not in conformance and not considered. Include copy of this form with next
communication to applicant.